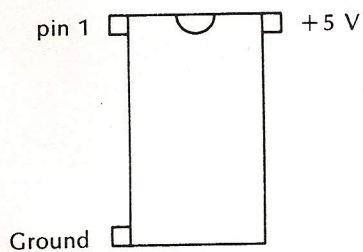


All integrated circuits are corner powered except those shown in the table below. Corner powered ICs have ground connected to the lower left pin, and +5 V connected to the upper right pin regardless of the total pin count. (eg., for a 16 pin DIP, ground is connected to pin 8 and +5 V is connected to pin 16)

Corner Power



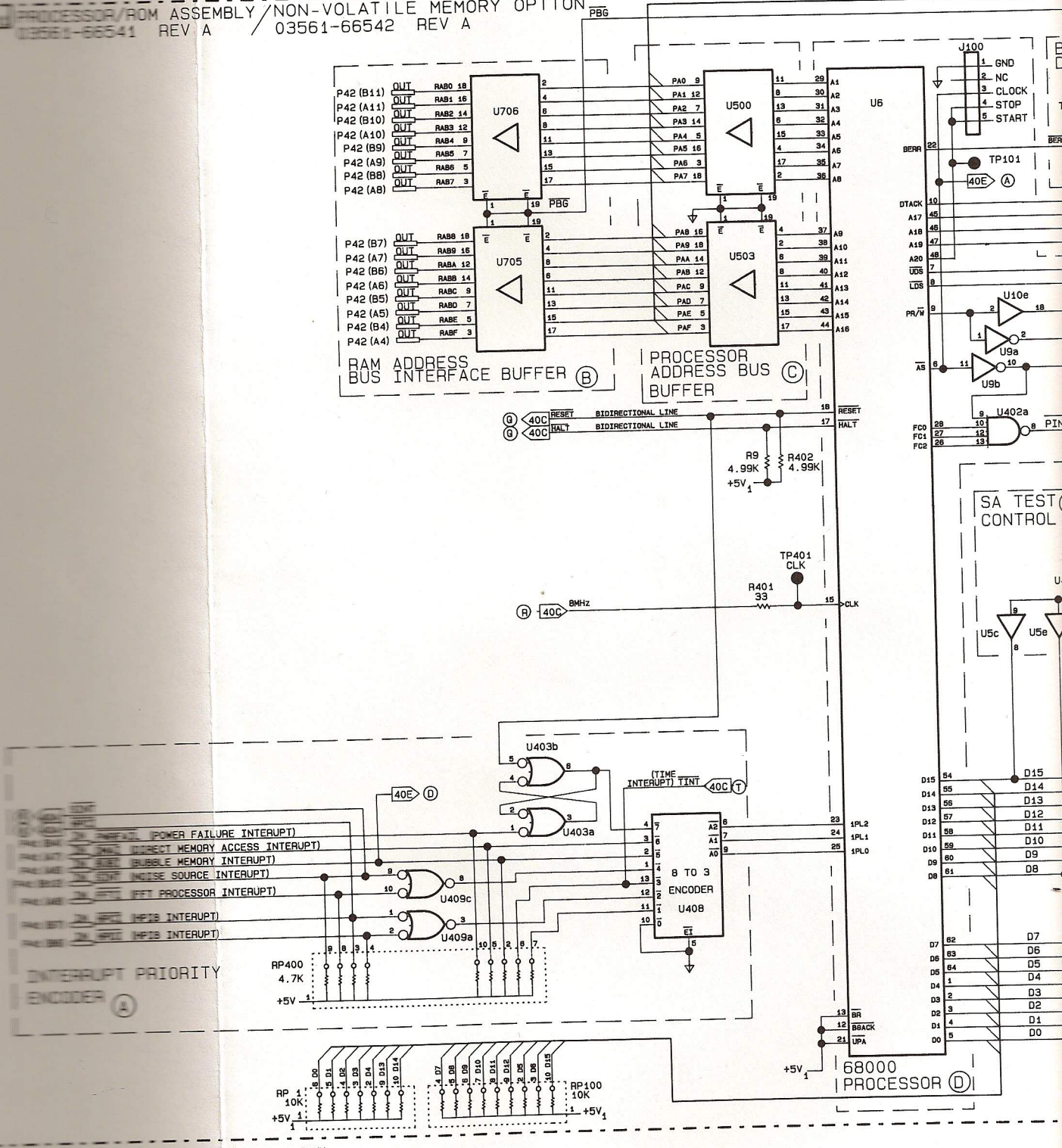
	+5V1	+5V4	GND
U1		5	12
U6	14,49	16,53	
U103		16	7

P/A41 PROCESSOR/ROM ASS  
03561-66541 REV

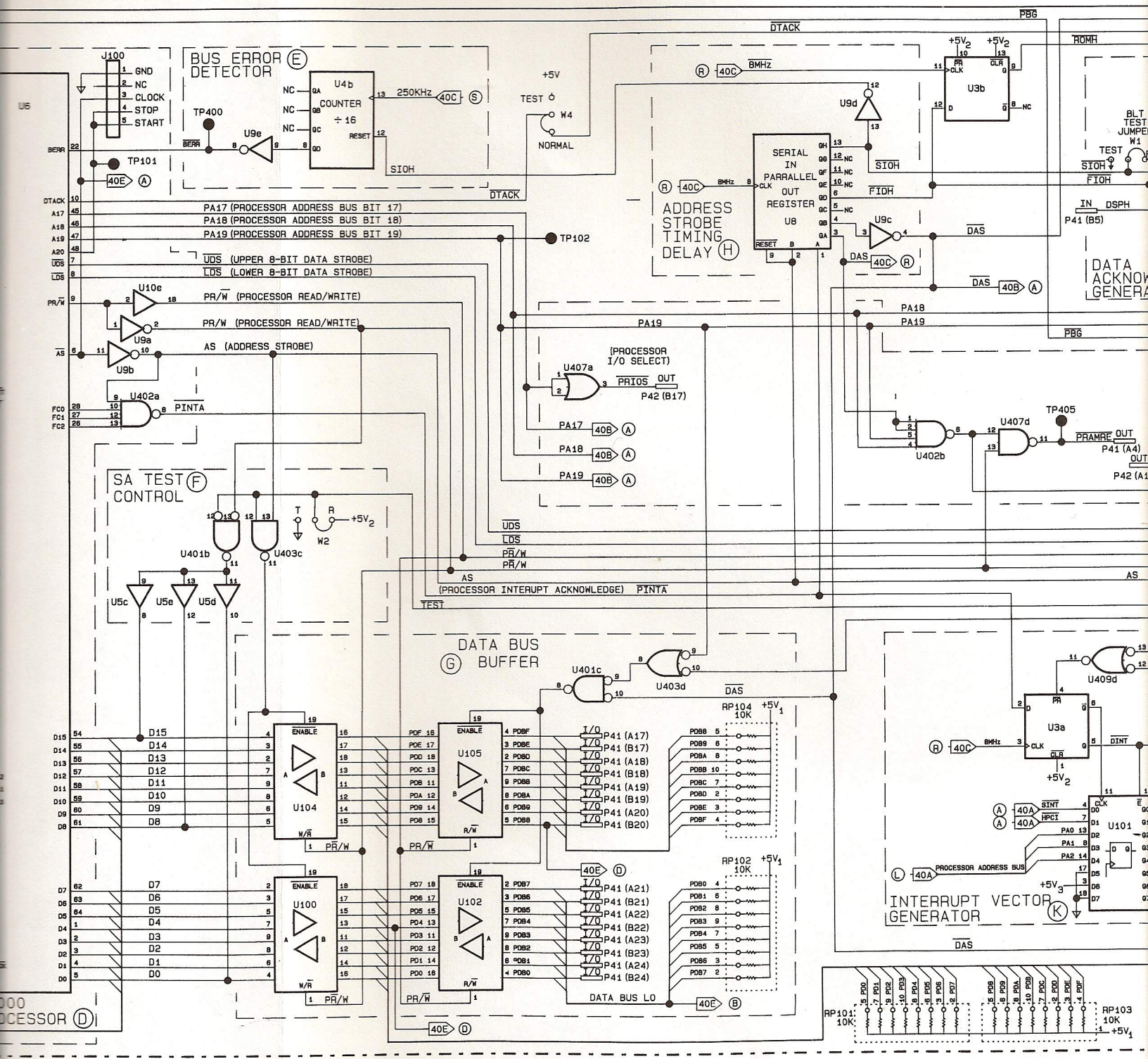
(K) <40A SINT  
(K) <40A HPCI  
P41 (B4) IN PWRFAIL (POWER FAIL)  
P41 (A7) IN DMAI (DIRECT MEMORY)  
P41 (A6) IN BUBI (BUBBLE MEMORY)  
P41 (B12) IN SINT (NOISE SOURCE)  
P41 (A8) IN FFTI (FFT PROCESSOR)  
P41 (B7) IN HPCI (HPIB INTERRUPT)  
P41 (B6) IN HPDI (HPIB INTERRUPT)

INTERRUPT PRIORITY  
ENCODER (A)

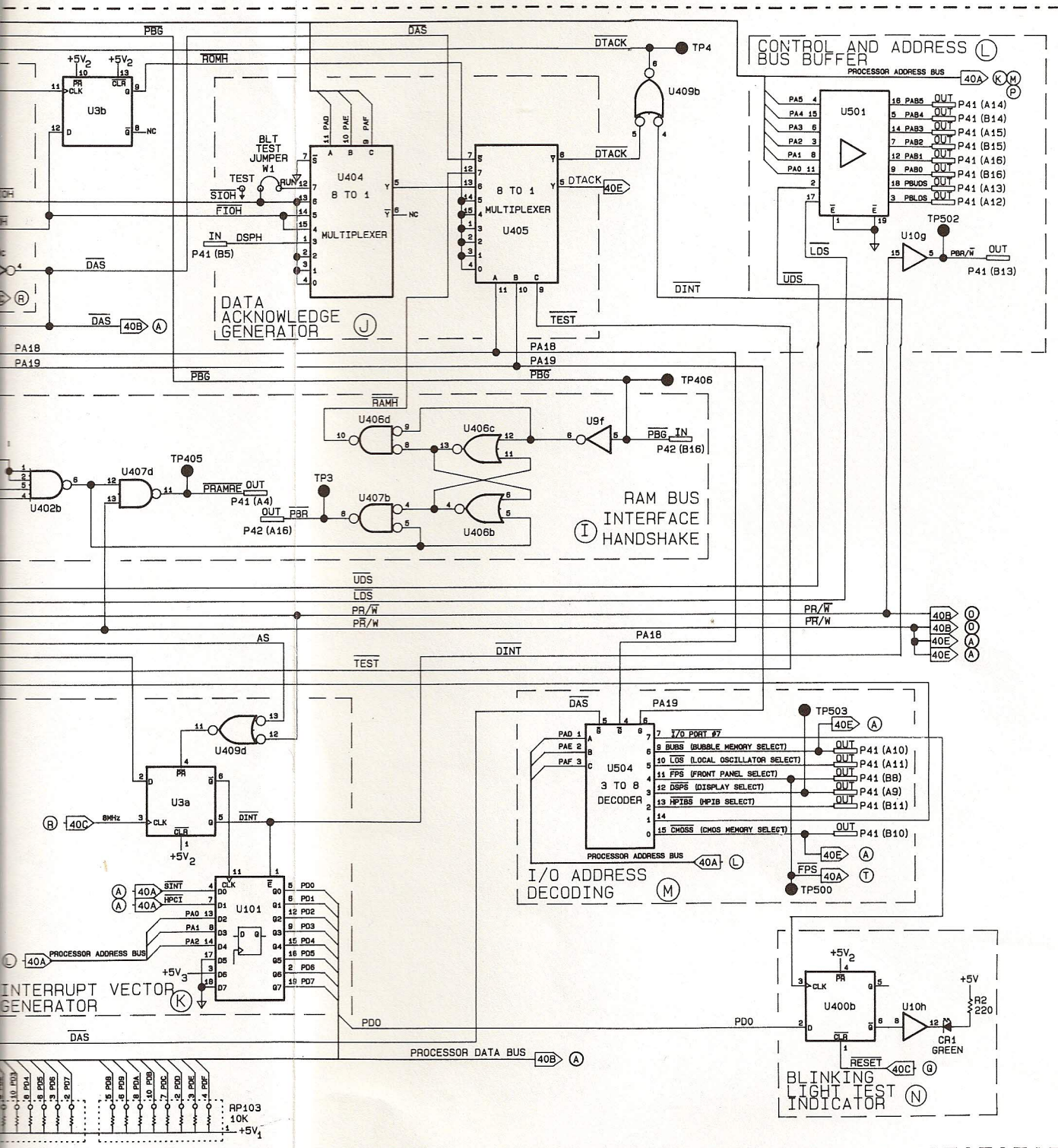
PROCESSOR/ROM ASSEMBLY / NON-VOLATILE MEMORY OPTION  
 03561-66541 REV A / 03561-66542 REV A











Schematic **A40A**  
 Processor/ROM Assembly  
 7-115/7-116